

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Serial No.: 10/797,876  
Applicant: Dip et al.  
Filing Date: March 10, 2004  
Art Unit: 2811  
Examiner: Colleen Matthews  
Title: **SILICON GERMANIUM SURFACE LAYER FOR HIGH-K  
DIELECTRIC INTEGRATION**  
Attorney Docket: TPS-007  
Confirmation No.: 5070

August 10, 2009

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**RESPONSE TO FINAL REJECTION**

This paper is responsive to the Office Action mailed June 10, 2009. The present response includes the following:

|                                  |                           |
|----------------------------------|---------------------------|
| Amendments to the Specification: | Page 2                    |
| Amendments to the Claims:        | Pages 3–7                 |
| Amendments to the Abstract:      | None                      |
| Amendments to the Drawings:      | Page 8                    |
| Remarks:                         | Pages 9–17                |
| Attachments:                     | Drawing Replacement Sheet |